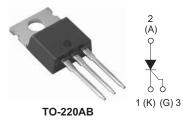


Vishay High Power Products

### Phase Control SCR, 12.5 A



PRODUCT SUMMARY		
V <sub>T</sub> at 8 A	1.2 V	
I <sub>TSM</sub>	140 A	
V <sub>RRM</sub>	800 V	

#### **DESCRIPTION/FEATURES**

The 12TTS08PbF High Voltage Series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology



RoHS\*

used has reliable operation up to 125 °C junction temperature.

Typical applications are in input rectification and crowbar (soft start) and these products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

This product has been designed and qualified for industrial level and lead (Pb)-free.

OUTPUT CURRENT IN TYPICAL APPLICATIONS				
APPLICATIONS	SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS			
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C, common heatsink of 1 °C/W	13.5	17	A	

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
I <sub>T(AV)</sub>	Sinusoidal waveform	8	٨	
I <sub>T(RMS)</sub>		12.5	A	
V <sub>DRM</sub> /V <sub>RRM</sub>		800	V	
I <sub>TSM</sub>		140	А	
V <sub>T</sub>	8 A, T <sub>J</sub> = 25 °C	1.2	V	
dV/dt		150	V/µs	
dl/dt		100	A/µs	
TJ	Range	- 40 to 125	°C	

VOLTAGE RATINGS				
PART NUMBER	V <sub>RRM</sub> , MAXIMUM PEAK VOLTAGE V	V <sub>DRM</sub> , MAXIMUM PEAK DIRECT VOLTAGE V	I <sub>RRM</sub> /I <sub>DRM</sub> AT 125 °C mA	
12TTS08PbF	800	800	1.0	

\* Pb containing terminations are not RoHS compliant, exemptions may apply

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average on-state current	I <sub>T(AV)</sub>	$T_{\rm C} = 108 ^{\circ}{\rm C}$ , 180° conduction, half sine wave		8	
Maximum RMS on-state current	I <sub>T(RMS)</sub>	$T_{\rm C} = 100$ C, 100 Colluu	cuon, nan sine wave	12.5	
Maximum peak, one-cycle,		10 ms sine pulse, rated V	$I_{\rm RRM}$ applied, T <sub>J</sub> = 125 °C	120	A
non-repetitive surge current	I <sub>TSM</sub>	10 ms sine pulse, no volt	age reapplied, $T_J = 125 \ ^{\circ}C$	140	
Maximum I <sup>2</sup> t for fusing	l <sup>2</sup> t	10 ms sine pulse, rated V	$I_{\rm RRM}$ applied, T <sub>J</sub> = 125 °C	72	A <sup>2</sup> s
Maximum i-tior fusing	1-1	10 ms sine pulse, no volt	age reapplied, T <sub>J</sub> = 125 °C	100	A-2
Maximum I <sup>2</sup> $\sqrt{t}$ for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied, $T_J$ = 125 °C		1000	A²√s
Maximum on-state voltage drop	V <sub>TM</sub>	8 A, T <sub>J</sub> = 25 °C		1.2	V
On-state slope resistance	r <sub>t</sub>	- T <sub>J</sub> = 125 °C		16.2	mΩ
Threshold voltage	V <sub>T(TO)</sub>			0.87	V
Maximum reverse and direct lookage surrent	1 /1	$T_J = 25 ^{\circ}C$	0.05		
Maximum reverse and direct leakage current	I <sub>RM</sub> /I <sub>DM</sub>	$T_J = 125 \ ^\circ C$	V <sub>R</sub> = Rated V <sub>RRM</sub> /V <sub>DRM</sub>	1.0	mA
Typical holding current	Ι <sub>Η</sub>	Anode supply = 6 V, resistive load, initial $I_T = 1 A$		30	IIIA
Maximum latching current	١L	Anode supply = 6 V, resistive load		50	
Maximum rate of rise of off-state voltage	dV/dt	T <sub>J</sub> = 25 °C		150	V/µs
Maximum rate of rise of turned-on current	dl/dt			100	A/µs

TRIGGERING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum peak gate power	P <sub>GM</sub>		8.0	W	
Maximum average gate power	P <sub>G(AV)</sub>		2.0	vv	
Maximum peak positive gate current	+ I <sub>GM</sub>		1.5	А	
Maximum peak negative gate voltage	- V <sub>GM</sub>		10	V	
Maximum required DC gate current to trigger	I <sub>GT</sub>	Anode supply = 6 V, resistive load, $T_J$ = - 65 °C	20	mA	
		Anode supply = 6 V, resistive load, $T_J$ = 25 °C	15		
		Anode supply = 6 V, resistive load, $T_J$ = 125 °C	10		
	V <sub>GT</sub>	Anode supply = 6 V, resistive load, $T_J$ = - 65 °C	1.2		
Maximum required DC gate voltage to trigger		Anode supply = 6 V, resistive load, $T_J$ = 25 °C	1		
		Anode supply = 6 V, resistive load, $T_J$ = 125 °C	0.7	V	
Maximum DC gate voltage not to trigger	$V_{GD}$				
Maximum DC gate current not to trigger	I <sub>GD</sub>			mA	

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Typical turn-on time	t <sub>gt</sub>	T <sub>J</sub> = 25 °C	0.8	
Typical reverse recovery time	t <sub>rr</sub>	T 105 %C	3	μs
Typical turn-off time	tq	T <sub>J</sub> = 125 °C	100	

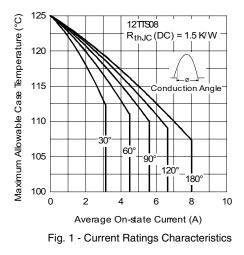


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THERMAL AND MECH	THERMAL AND MECHANICAL SPECIFICATIONS				
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 40 to 125	°C
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation	1.5	
Maximum thermal resistance, junction to ambient		R <sub>thJA</sub>		62	°C/W
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.5	
Approvimeto weight				2	g
Approximate weight				0.07	oz.
Mounting torque	minimum			6 (5)	kgf · cm
Mounting torque	maximum			12 (10)	(lbf ⋅ in)
Marking device			Case style TO-220AB	12T	TS08

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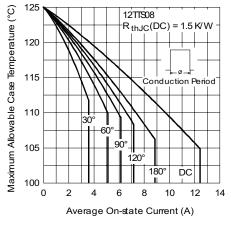


Fig. 2 - Current Ratings Characteristics

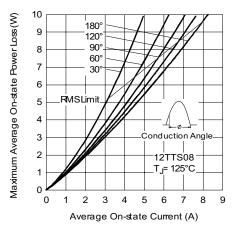


Fig. 3 - On-State Power Loss Characteristics

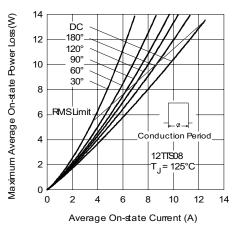


Fig. 4 - On-State Power Loss Characteristics

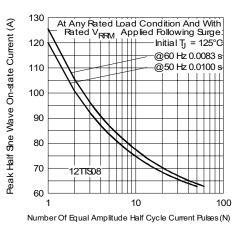


Fig. 5 - Maximum Non-Repetitive Surge Current

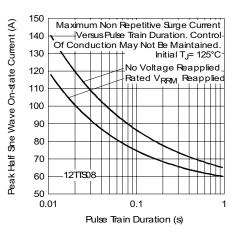


Fig. 6 - Maximum Non-Repetitive Surge Current



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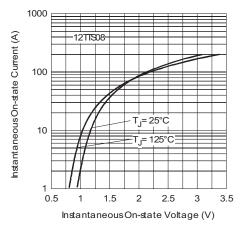


Fig. 7 - On-State Voltage Drop Characteristics

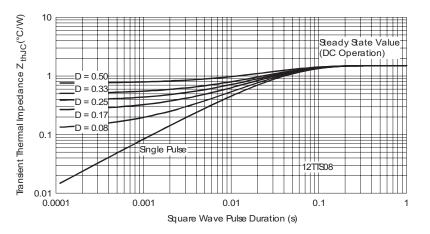
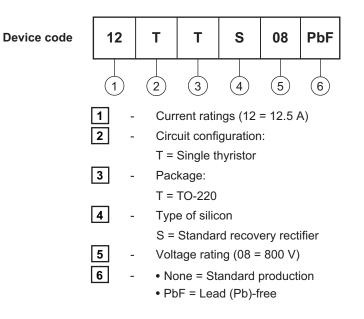


Fig. 8 - Thermal Impedance Z<sub>thJC</sub> Characteristics

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### ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95222				
Part marking information	http://www.vishay.com/doc?95225			



Vishay

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